

### REMARKS

We have addressed the informalities noted by the Examiner in his objections to claims 1, 4, and 11. In particular, we have canceled claims 1 and 11 and have amended claim 4 to replace "PLL circuit" with Phase-locked loop (PLL) circuit.

We acknowledge the Examiner's indication that claims 2, 7-9, and 12 would be allowable if rewritten in independent form including all of the limitations recited in base and intervening claims. Accordingly, we have amended dependent claims 2 and 12 to be in independent form and to include the features recited in independent claims 1 and 11 (now canceled), respectively. We have also amended independent claim 4 to include the limitations recited in dependent claim 89 (now canceled). We have also amended dependent claims 3 and 13 so as not to depend from a canceled claim.

### Prior Art Rejections

The Examiner rejected claims 1, 3, 4, 10, , and 11 as anticipated by Graham (U.S. 5,072,195). We submit that this rejection is moot in view of our having canceled claims 1 and 11 and our amendment of claim 4. We submit that dependent claims 3 and 10 are patentable for at least the same reasons that independent claims 1 and 4 are patentable.

The Examiner also rejected dependent claim 5 as unpatentable over Graham in view of Lee (U.S. 5,734,301) and dependent claim 6 as unpatentable over Graham in view of Lee and further in view of Yoshizawa (U.S. 5,909,474). We submit however that dependent claims 5 and 6 are patentable for at least the same reasons that claim 4 is patentable. Applicant asks that all claims be allowed.

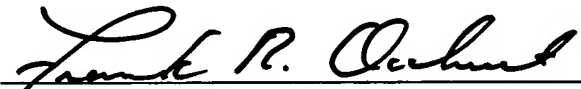
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Respectfully submitted,

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